



Department of Physics
School of Mathematics and Physical Sciences
Dr. Hari Singh Gour Vishwavidyalaya, Sagar (MP)
(A Central University)

National Webinar
On
Optimized Digital Logic
Circuit Design and Testing

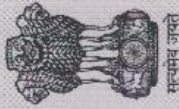
Expert:
Dr. Fazal Noorbhasha, K. L. University
Guntur AP

Convener :
Dr. Ashish Verma
Professor

Coordinator :
Prof. Ranveer Kumar
Head

www.dhsgsu.ac.in June 20, 2020
Time: 3.00 pm

Zoom Id: 3656722262
Password:: 828708



National Webinar
on
Optimized Digital Logic Circuit Design and Testing

(June 20, 2020, 3:00 p.m. to 4:00 p.m.)

(Organized by)

Department of Physics
School of Mathematical and Physical Sciences
Dr. Harisingh Gour Vishwavidyalaya, Sagar [M.P.]
(A Central University)

Patron: Prof. R.P. Tiwari, Hon'ble Vice Chancellor

Co Patron: Prof. Dwakar Shukla, Dean, SMPS

Expert: Dr. Fazal Noorbasha, Associate Professor, Dept. of ECE, KL University, Guntur, A.P.

Coordinator: Prof. Ranveer Kumar, Head, Dept of Physics

Convener: Prof. (Dr.) Ashish Verma

6/10/2020

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National Webinar
On

OPTIMIZED DIGITAL LOGIC DESIGNS AND TESTING

Department of Physics , School of Mathematical & Physical Sciences, Dr. Harisingh Gour Vishwavidyalaya (A central University) , Sagar, M.P., 470003 is organising a national webinar

On 20th June 2020 at 3pm to 4pm.

Expert : Dr. Fazal Noorbhasha
Associate Professor
Department of Electronics & Communication Engineering
K.L. University, Guntur, A.P.

Registration : free registration (seats limited to 100)
Faculty
M.Sc.(Physics & Electronics) students
B.E. And M.E. Of ECE students
Research Scholars
(e-certificates will be given to all the participants)

Registration link : <https://docs.google.com/forms/d...>Read more